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In the Abstract:

The present invention also provides a A line inversion drive circuit for a thin film transistor liquid crystal display is provided. The line inversion drive circuit comprises includes a clock controller and a data line driver. The clock controller includes a data inversion circuit for receiving a data signal and a clock control device; the data inversion circuit is coupled to the clock control device; the data inversion circuit which is responsive to an inversion control signal determines whether to invert the data signal and outputs a display signal. The data line driver, coupled to the data inversion device, is for receiving a group of reference voltages; the data line driver is responsive to the group of reference voltages and the display signal drives a plurality of data lines of the thin film transistor liquid crystal display. The present invention uses a A single group of reference voltages is used only for both non-inverse and inverse data signals.